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**DATE:** November 11, 2004

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**TOTAL NUMBER OF PAGES 9 (including this page)**

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**Docket No.:** SC12810TK

**Applicant:** Bennett A. Joiner et al.

**Serial No.:** 10/694,146

**Art Unit:** 2811

**Filed:** October 27, 2003

#### ALL ITEMS MARKED WITH AN "X" ARE INCLUDED:

1.	<input checked="" type="checkbox"/>	1 page Facsimile Cover Sheet
2.	<input checked="" type="checkbox"/>	4 page Supplemental Declaration
3.	<input checked="" type="checkbox"/>	4 page Preliminary Amendment

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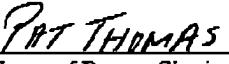
## UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANTS Bennett A. Joiner et al. GROUP ART UNIT: 2811  
APPLN. NO.: 10/694,146 EXAMINER: Junghwa M. Im  
FILED: September 29, 2004  
TITLE: ELECTROMAGNETIC NOISE SHIELDING IN SEMICONDUCTOR  
PACKAGES USING CAGED INTERCONNECT STRUCTURES

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AMENDMENT UNDER 37 C.F.R. 1.115**

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Sir:

This paper is being mailed prior to receipt of a first substantive Office Action. Further examination and reconsideration are respectfully requested in view of the election made below.

**FEES**

No additional fees are believed to be owed in connection with this response. However, Applicants continue to authorize the payment of any fees owed in connection with this application from Deposit Account No. 503079 (Freescale Semiconductor, Inc.), or credit Deposit Account No. 503079 for any refunds.